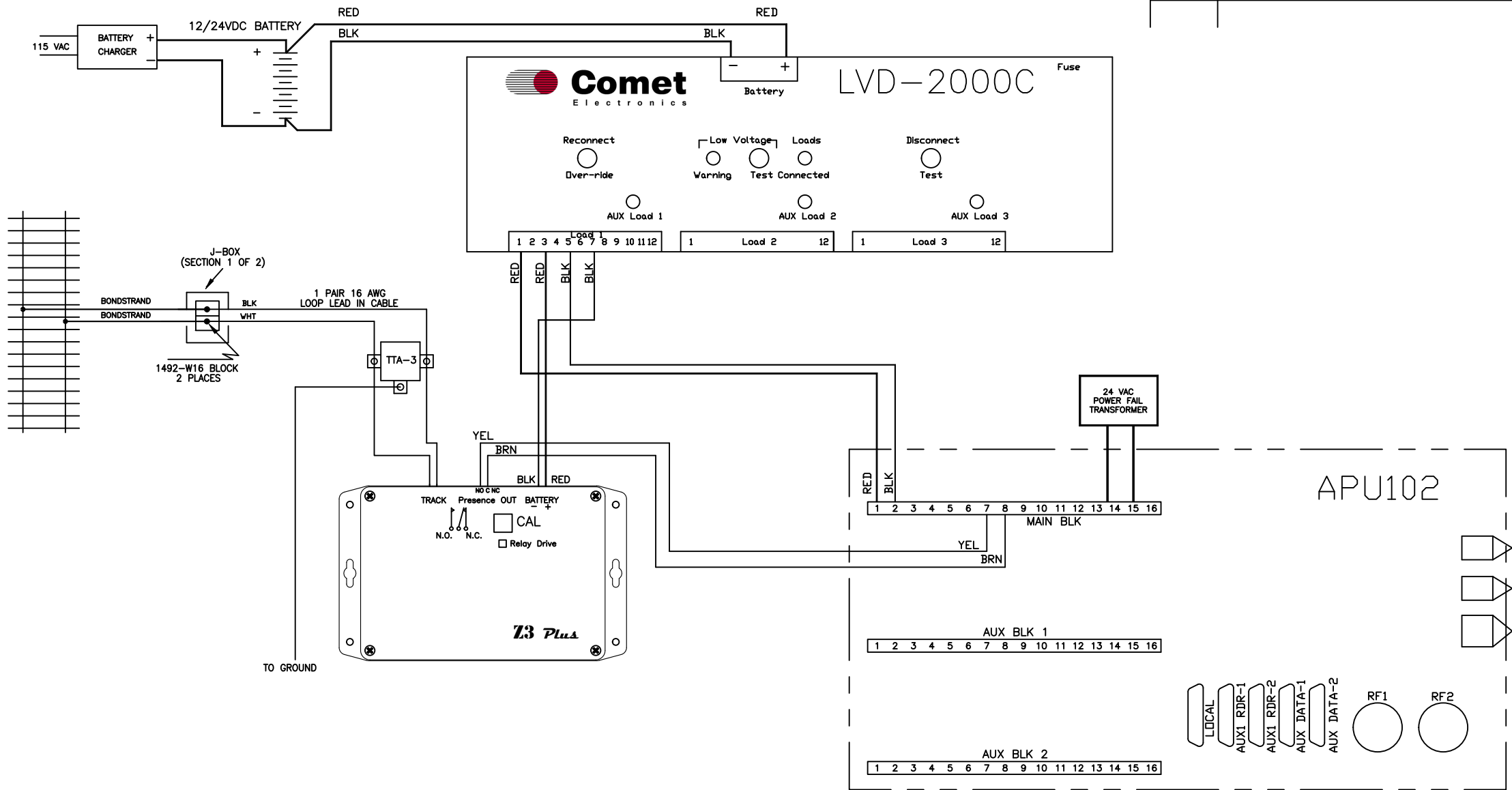




APU102 TERMINAL BLOCK CONNECTIONS

MAIN TERMINAL BLOCK															
1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
+VDC PWR	-VDC COM	WDA +	WDA -	WDB +	WDB -	PRES IN	GND	PRES OUT	GND	24VAC IN	24VAC IN	REM-LOCK+	GND	DT PRES. SYNC IN	DT PRES. OUT
POWER FAIL TRANSFORMER															
DOUBLE TRACK															
1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
+12 FUSED	+5 FUSED	PROGRAMMABLE	GND	WDC +	WDC -	CLOCK SYNC +	CLOCK SYNC -	WATCHDOG PULSE	GND			REM-LOCK+	REM-LOCK-	V-EXT	GND
AUX BLK 1 - PIN 3 PROGRAMMABLE AS:															
1. CLOCK SYNC INPUT(SECONDARY)															
2. EXTERNAL, TIMED PRESENCE(SECONDARY)															
3. REMOTE TRACK PRESENCE															
LVD2000 USE REQUIRES VIO.CFG FILE WITH DC LOW ALARM CONFIGURED															
1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
WDD +	WDD -	LVD2000 DC FAIL +	LVD2000 DC FAIL -	TIER 2 ANT 0 +	TIER 2 ANT 0 -	SP IN 0	GND	LVD2000 CONTROL +	LVD2000 CONTROL -	SP OUT 2+	SP OUT 2-	TIER 2 ANT 1 +	TIER 2 ANT 1 -	TIER 2 ANT 0 +	TIER 2 ANT 0 -
VALID TAG															
RF CONTROL															

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DRAWN DDC	DATE 01.16.2020
ENGINEER	DATE
APPROVED	DATE



2405 Nicholson Ave.
Kansas City, MO 64120

DWG NAME TRACK PRESENCE WITH ZEPIC Z3		DWG NO. 70-0274-06	PARTS SHOWN	REV A
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